

09/555625

		Subclass
	Class	ISSUE CLASSIFICATION

- FILED UNDER 35 U.S.C. §71

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## **U.S. UTILITY Patent Application**

 <b>O.I.P.E.</b> SCANNED <u>JM<sup>1</sup></u> <u>Q.A.</u> 	<b>PATENT DATE</b> $\vdots$
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APPLICATION NO. 09/555625	CONT/PRIOR D F	CLASS 438	SUBCLASS 257	ART UNIT 1205	EXAMINER H W LPT 10/10/01
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**WANTS** Makoto Sasaki  
Gee Sung Chae

Wiring, thin-film transistor substrate with the wiring, method of manufacture thereof, and liquid crystal display device

PTO-2040  
12/89

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS	CLAIMS ALLOWED			
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.			<b>NOTICE OF ALLOWANCE MAILED</b>		
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____	(Assistant Examiner) _____  _____		<b>BEST AVAILABLE COPY</b>  <b>ISSUE FEE</b>		
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) _____  _____		<b>Amount Due</b> _____ <b>Date Paid</b> _____		
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